

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: See Attached Schedule A

Serial No.: See Attached Schedule A

Filed: See Attached Schedule A

For: See Attached Schedule A

REVOCATION AND POWER OF ATTORNEY

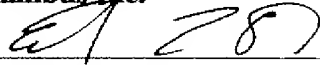
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R:

Rambus Inc., owner of the entire right, title and interest in, to and under the invention described and claimed in the above-identified patent application hereby revokes all previous powers of attorney and appoints Morgan, Lewis & Bockius LLP, customer no. **38426**, and each of them, its attorneys, to prosecute this application, and to transact all business in the Patent and Trademark Office connected therewith.

In addition, the undersigned assignee also appoints Kent R. Richardson (Reg. No. 39,443), Erik L. Oliver (Reg. No. 46,296), Paul M. Anderson (Reg. No. 39,896), Lance Kreisman (Reg. No. 39,256), Jose G. Moniz (Reg. 50,192), Paula J. Tostado (Reg. No. 40,691), and Jamie Zhang (Reg. No. 51,167) of Rambus Inc., to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Please direct all future correspondence to Customer Number **38426**, Morgan, Lewis & Bockius LLP, located at 2 Palo Alto Square, 3000 El Camino Real, Suite 700, Palo Alto, California 94306, and direct all telephone calls to Morgan, Lewis & Bockius LLP at (650) 843-4000.

Assignee:	Rambus Inc.
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SCHEDULE A**[U.S.]**

Serial No./ Patent No:	Filing Date/ Issue Date:	Title:	First Named Inventor:	Docket No:
09/353,547 6,839,393 B1	07/14/1999 01/04/2005	Apparatus and Method for Controlling a Master/Slave System Via Master Device Synchronization	Stefanos Sidiropoulos	60809-0022-US
10/963,828	10/11/2004	Apparatus and Method for Controlling a Master/Slave System Via Master Device Synchronization	Stefanos Sidiropoulos	60809-5031-US
10/954,941	09/30/2004	Integrated Circuit Memory System Having Dynamic Memory Bank Count and Page Size	Steven Woo	60809-5114-US
10/456,303 7,061,273 B2	06/06/2003 06/13/2006	Method and Apparatus for Multi- Mode Driver	Yueyong Wang	60809-5115-US
11/385,234	03/20/2006	Method and Apparatus for Multi- Mode Drive	Yueyong Wang	60809-5116-US
09/686,744 6,349,050 B1	10/10/2000 02/19/2002	Methods and Systems for Reducing Heat Flux in Memory Systems	Steven Woo	60809-5117-US
09/989,953 6,552,948 B2	11/21/2001 04/22/2003	Methods and Systems for Reducing Heat Flux in Memory Systems	Steven Woo	60809-5118-US
10/361,109 6,721,226 B2	02/07/2003 04/13/2004	Method and Systems for Reducing Heat Flux in Memory Systems	Steven Woo	60809-5119-US
10/770,150	02/02/2004	Method and Systems for Reducing Heat Flux in Memory Systems	Steven Woo	60809-5120-US

